

FIG. 1

2 / 29

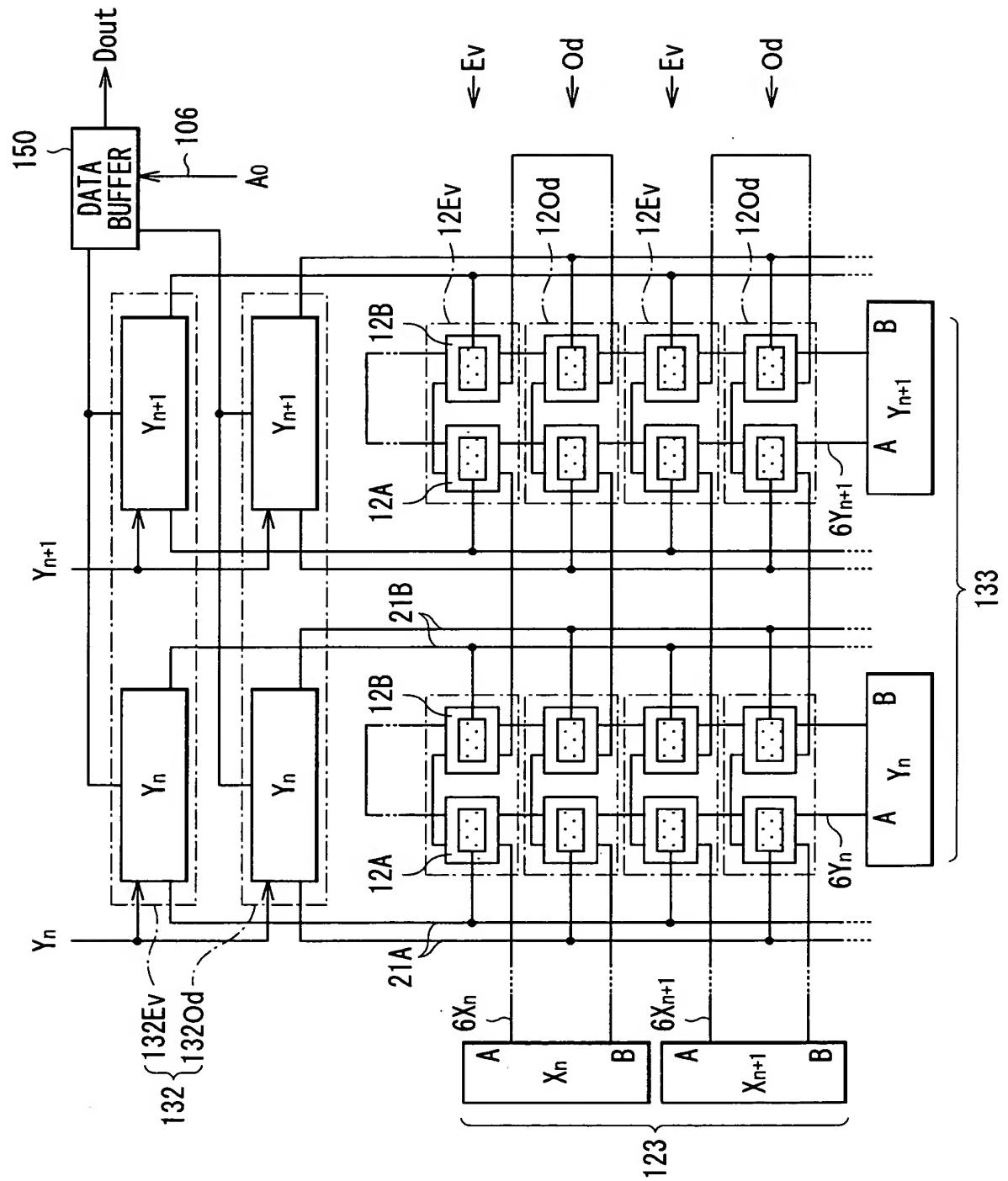


FIG. 2

10/550201

3/29

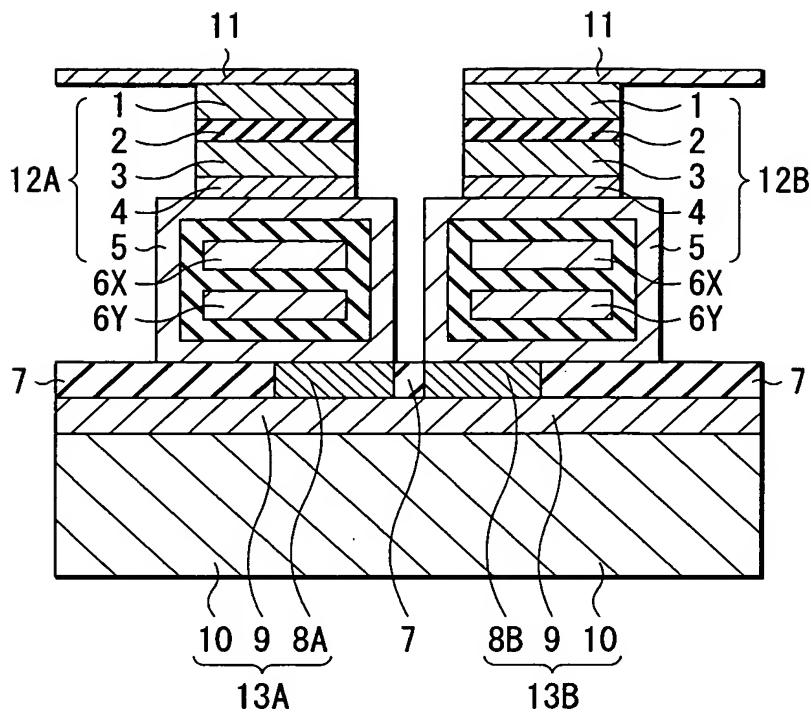


FIG. 3

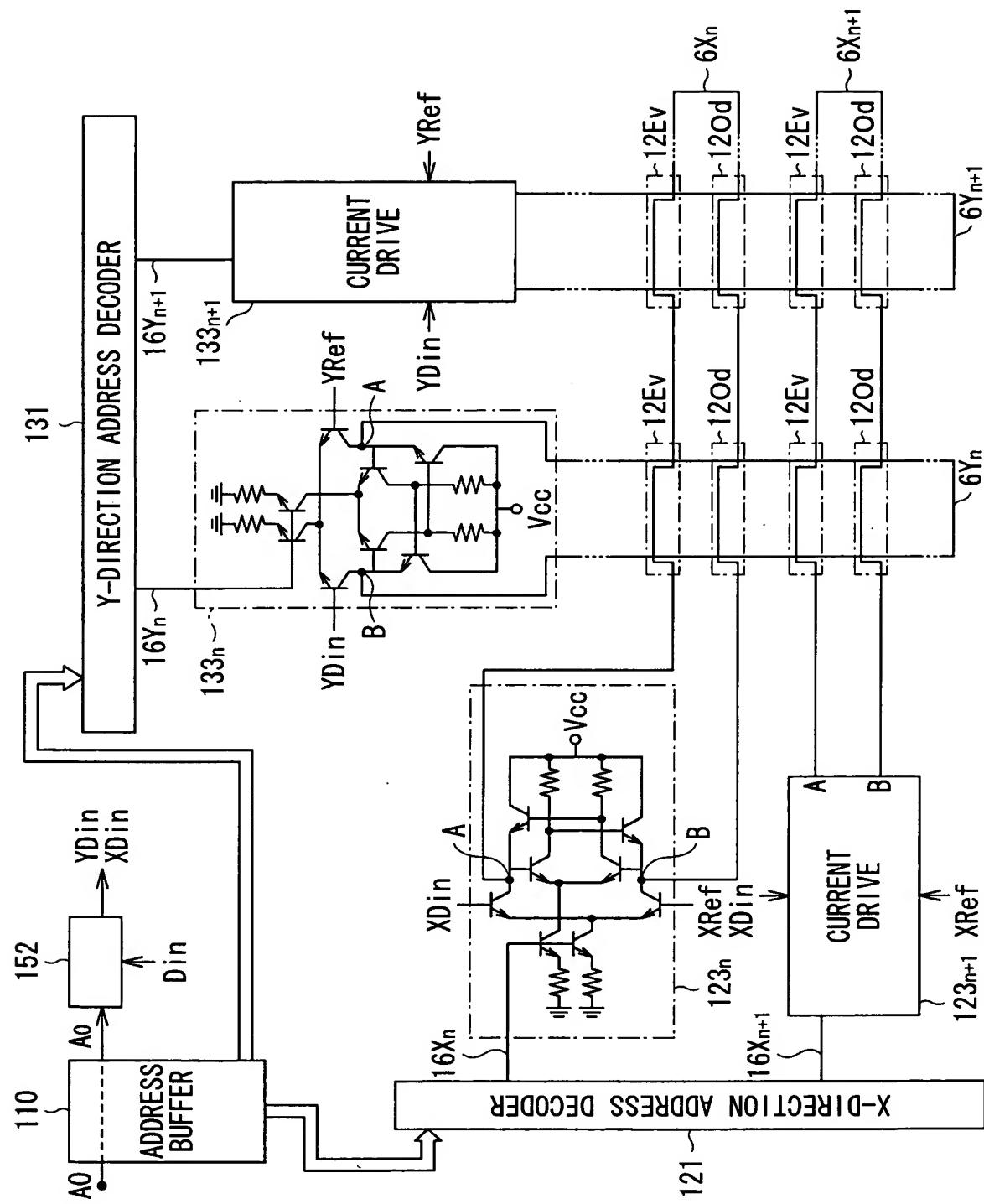
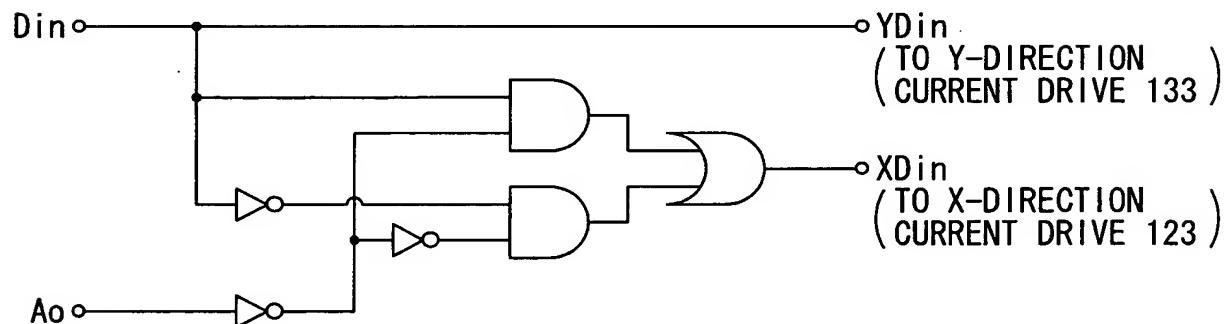


FIG. 4

5/29

	Ao	Din	XDin	YDin
WRITE "1" TO MEMORY CELL 12Ev	0	1	1	1
WRITE "0" TO MEMORY CELL 12Ev	0	0	0	0
WRITE "1" TO MEMORY CELL 12Od	1	1	0	1
WRITE "0" TO MEMORY CELL12Od	1	0	1	0

FIG. 5



152

FIG. 6

6/29

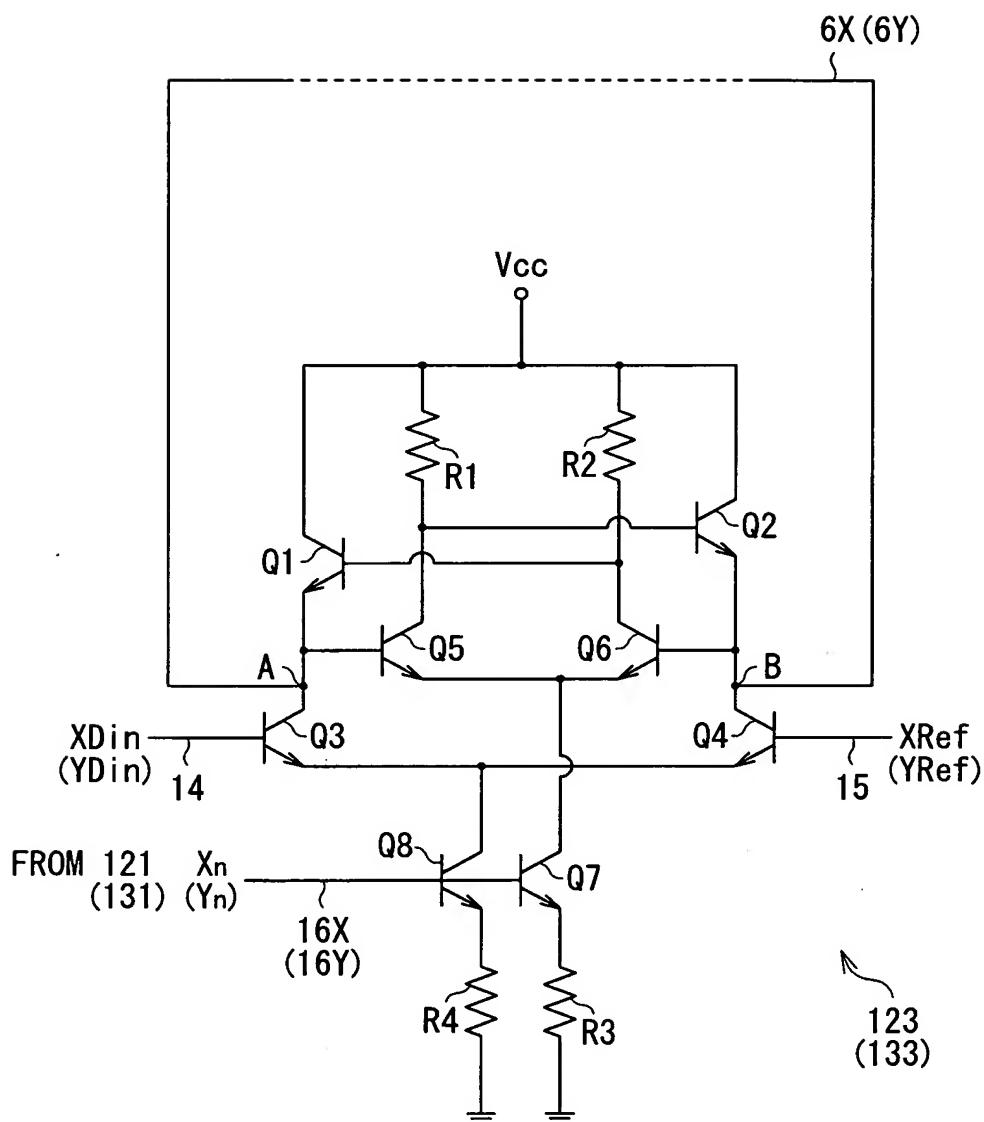


FIG. 7

7 / 29

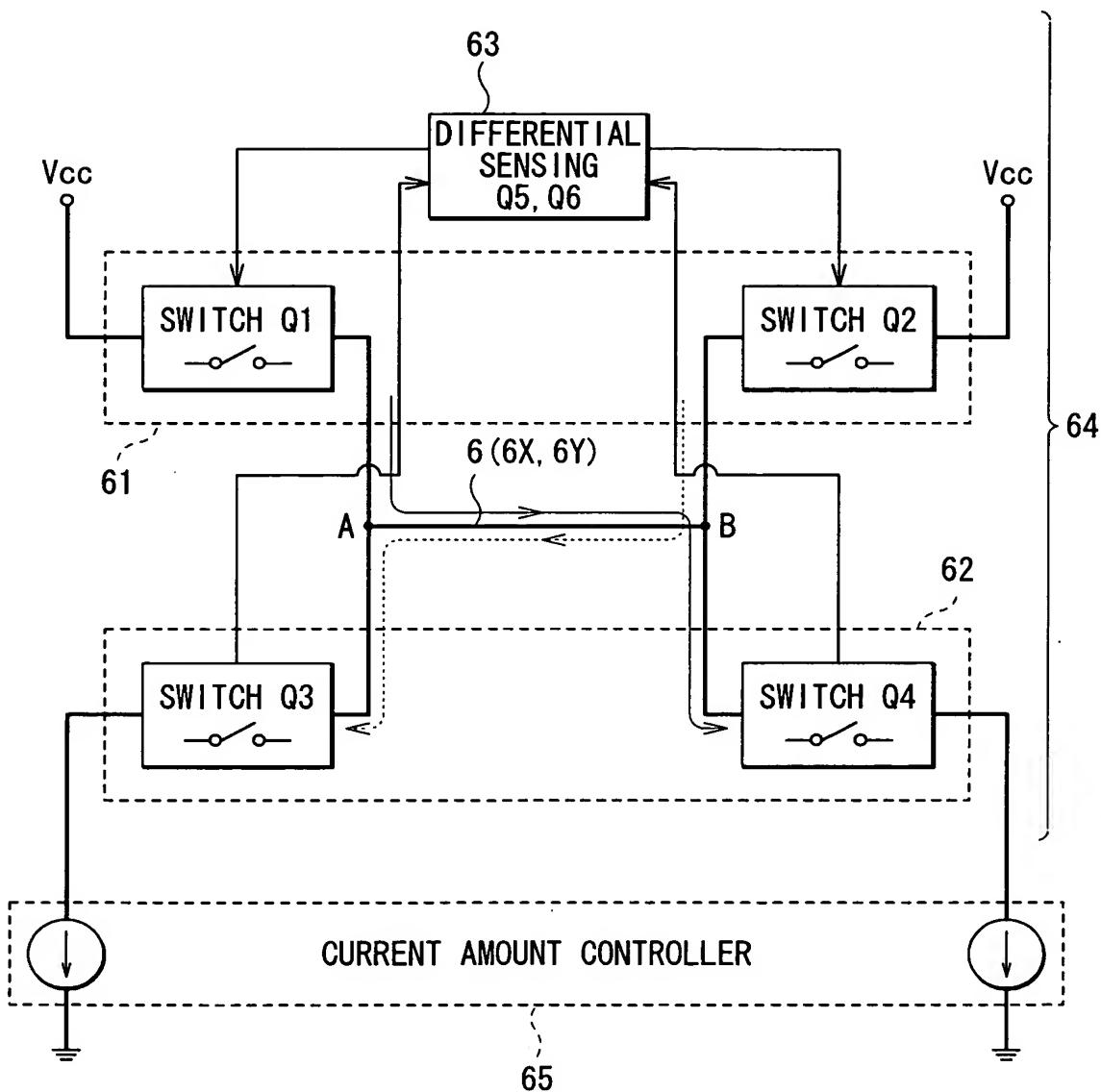


FIG. 8

8 / 29

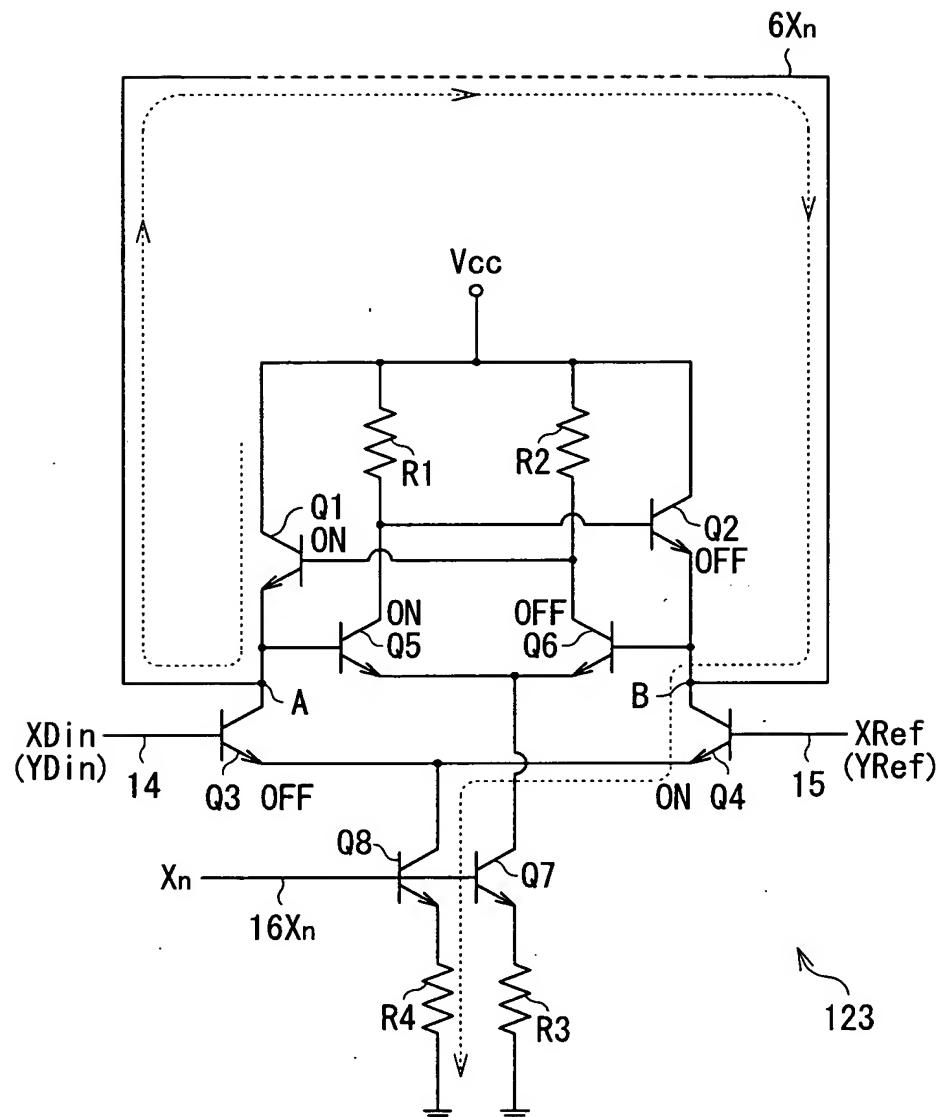


FIG. 9

9 / 29

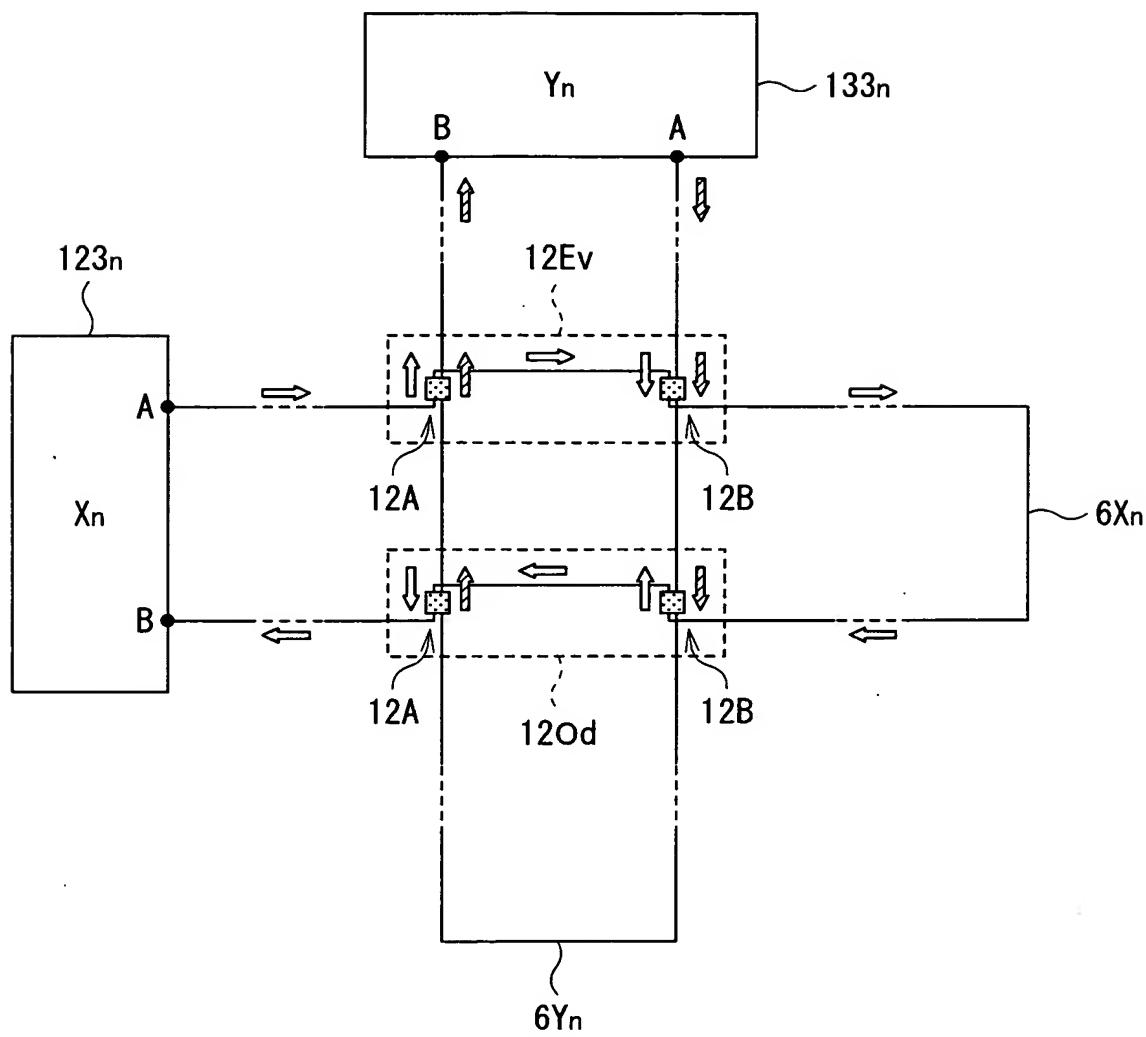


FIG. 10

10/550201

10 / 29

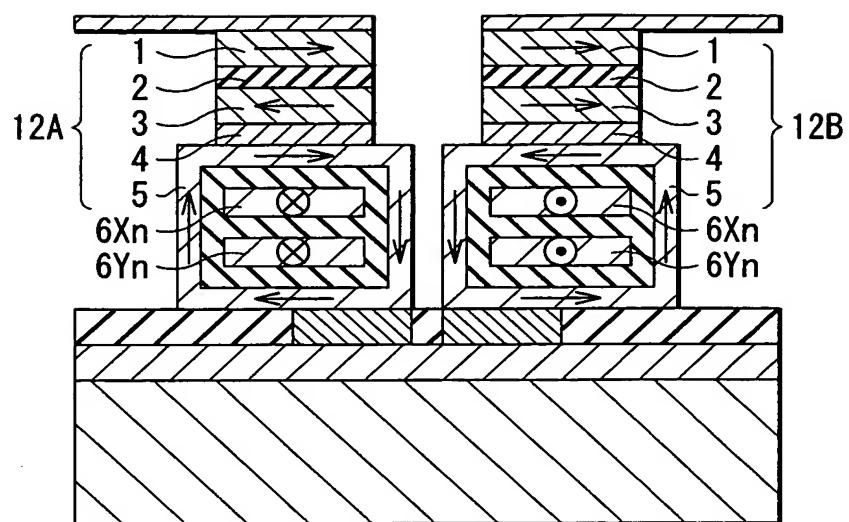


FIG. 11

10/550201

11/29

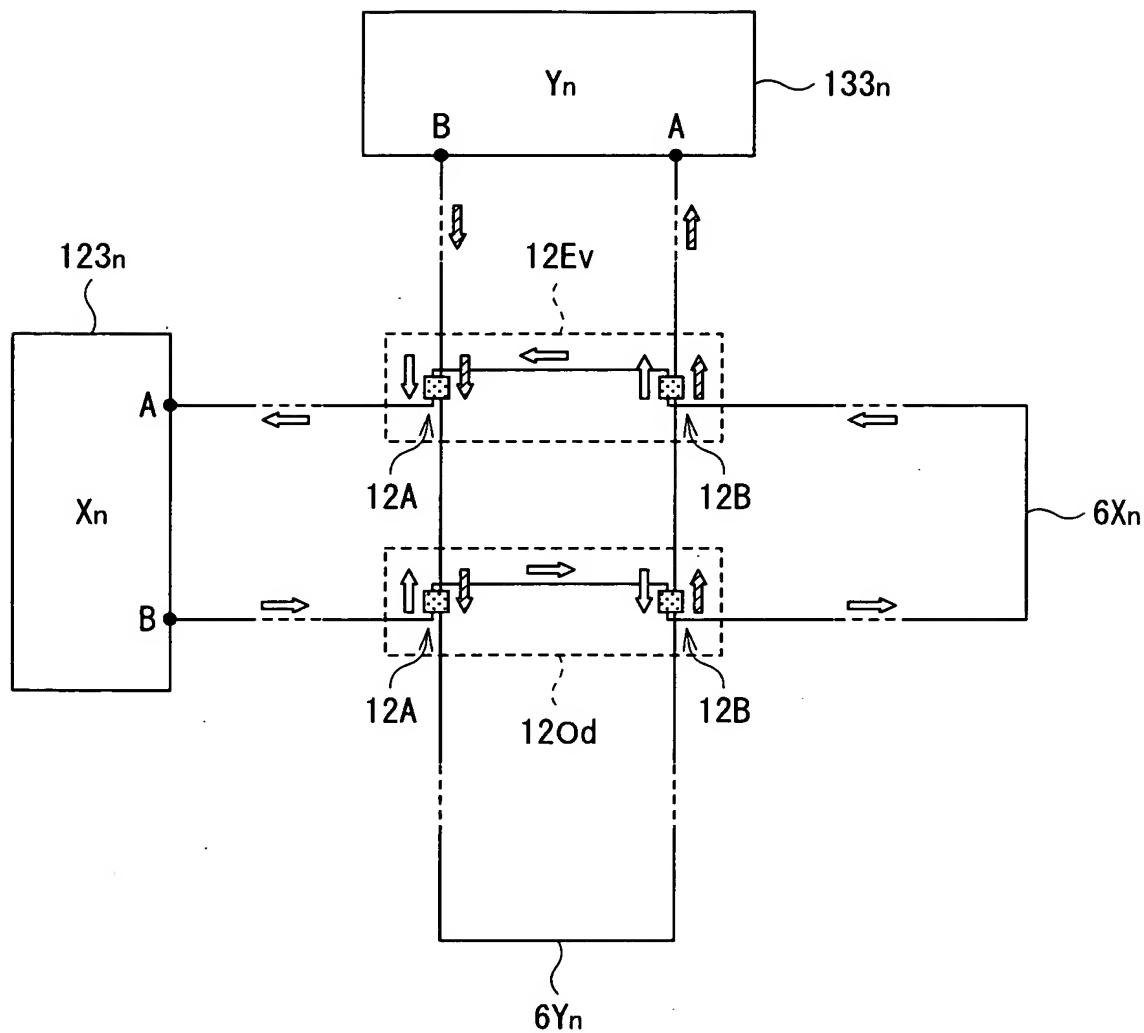


FIG. 12

10/550201

12/29

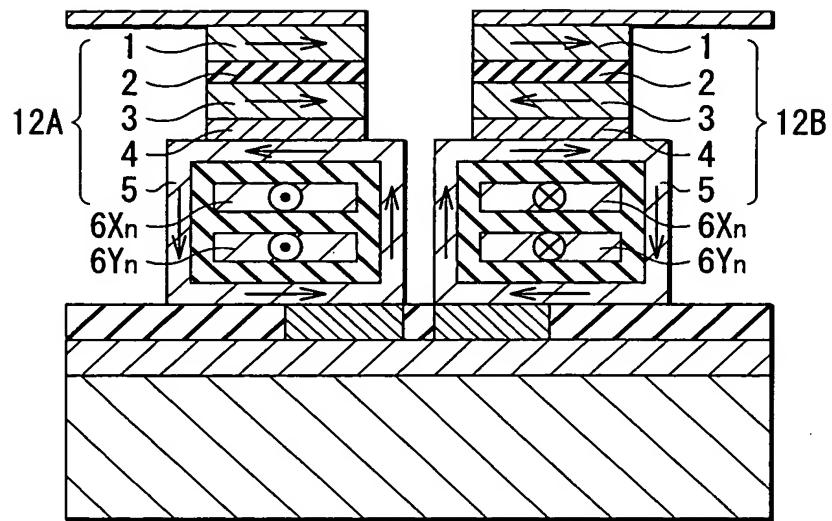


FIG. 13

10/550201

13 / 29

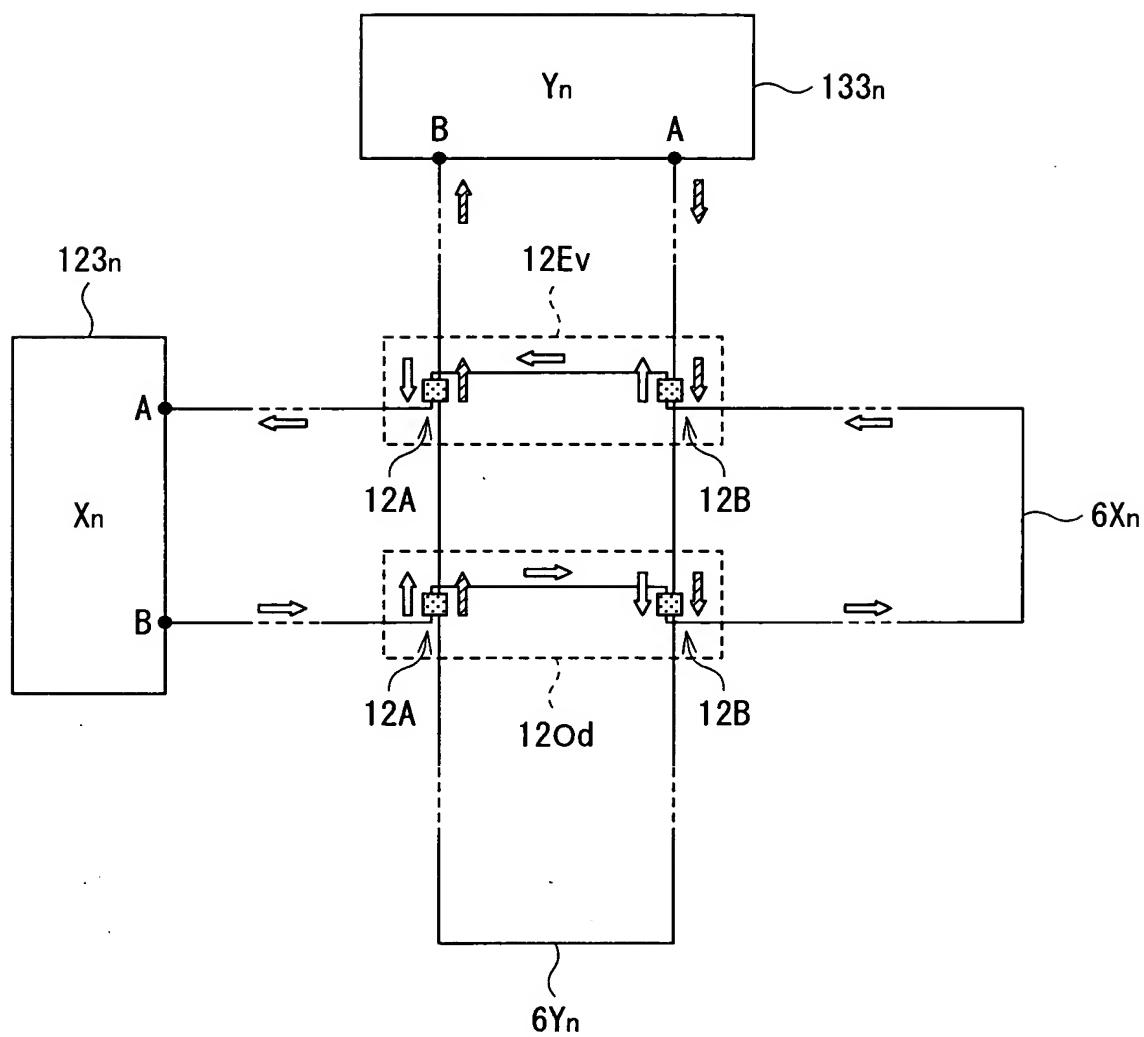


FIG. 14

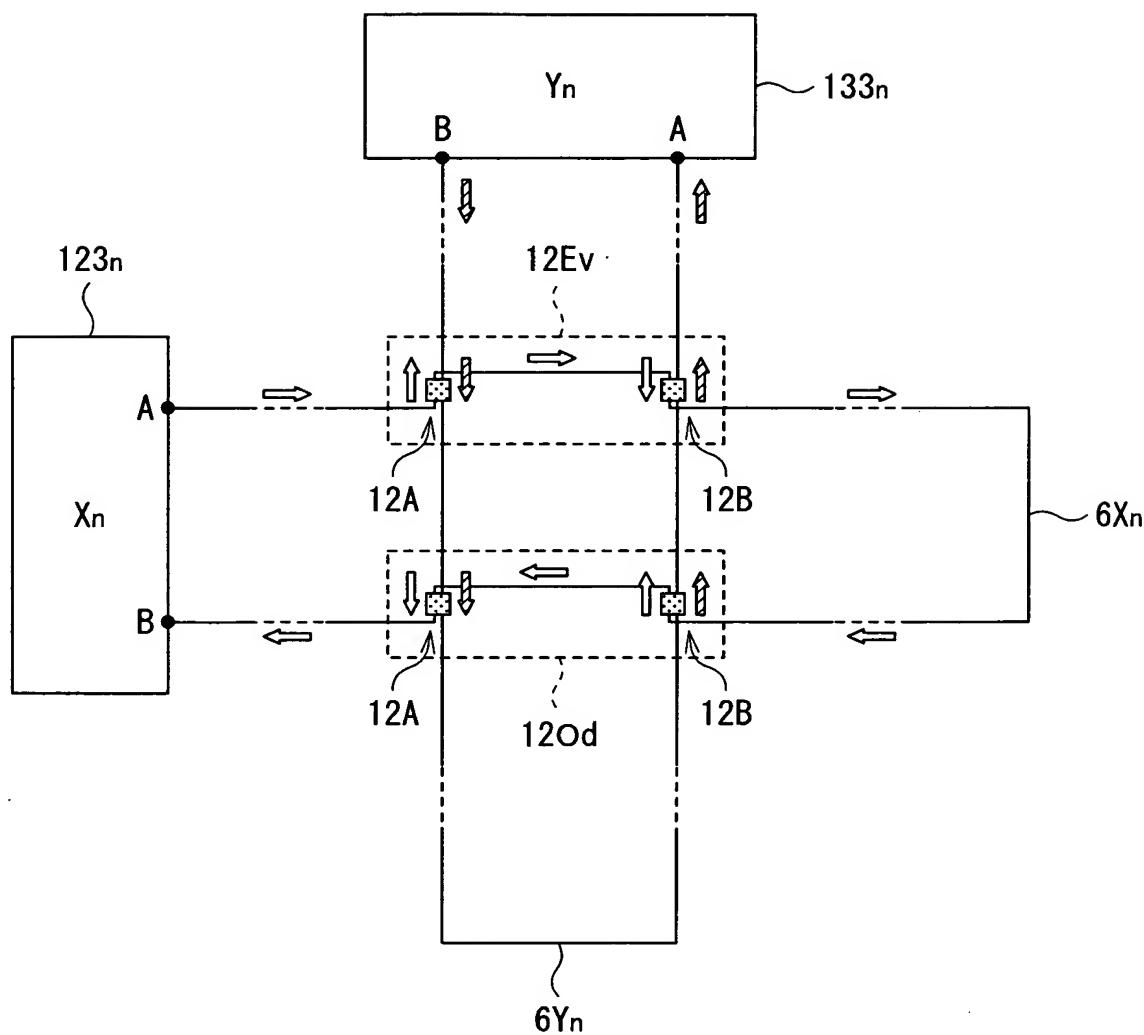


FIG. 15

15 / 29

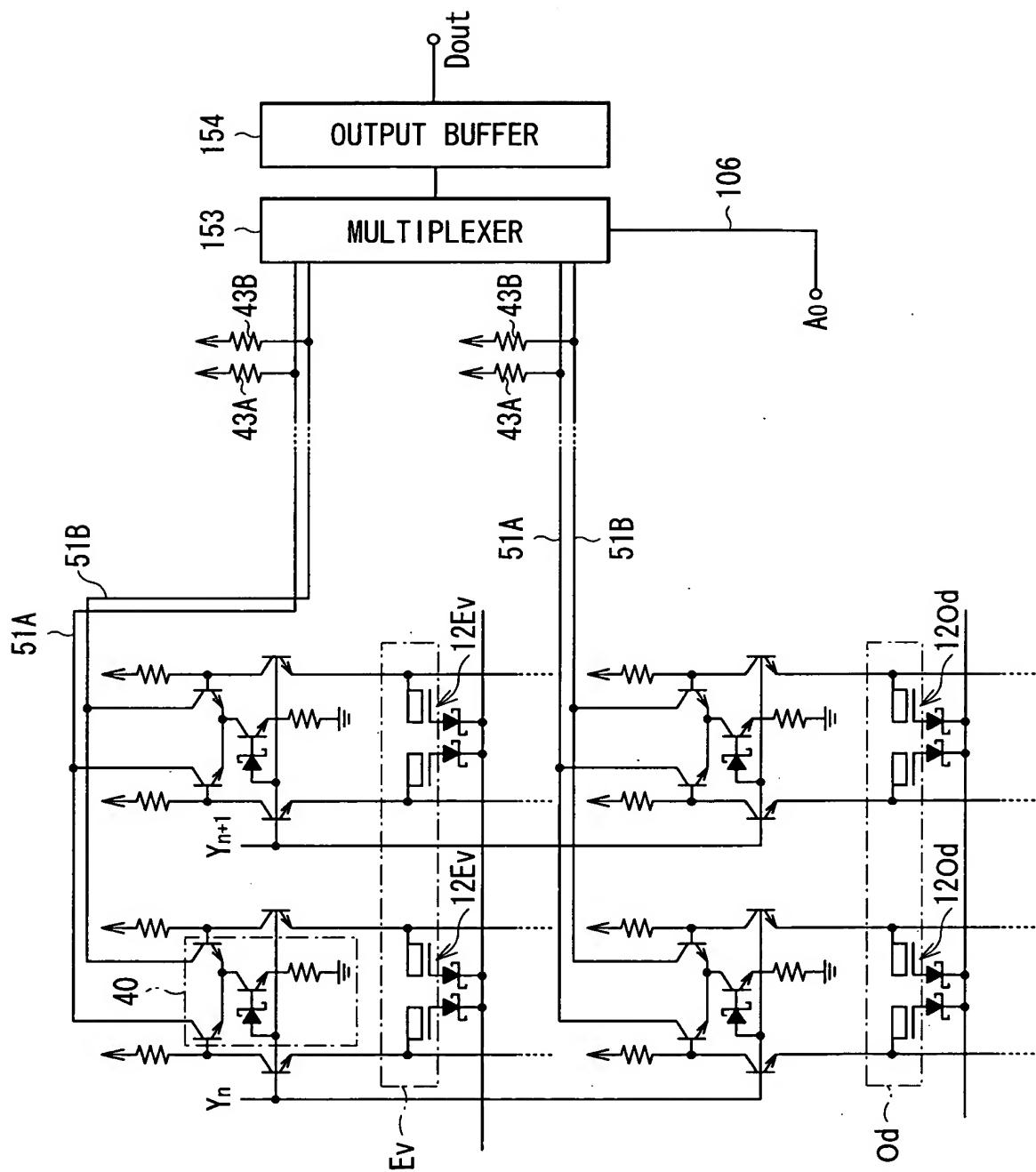


FIG. 16

10/550201

16 / 29

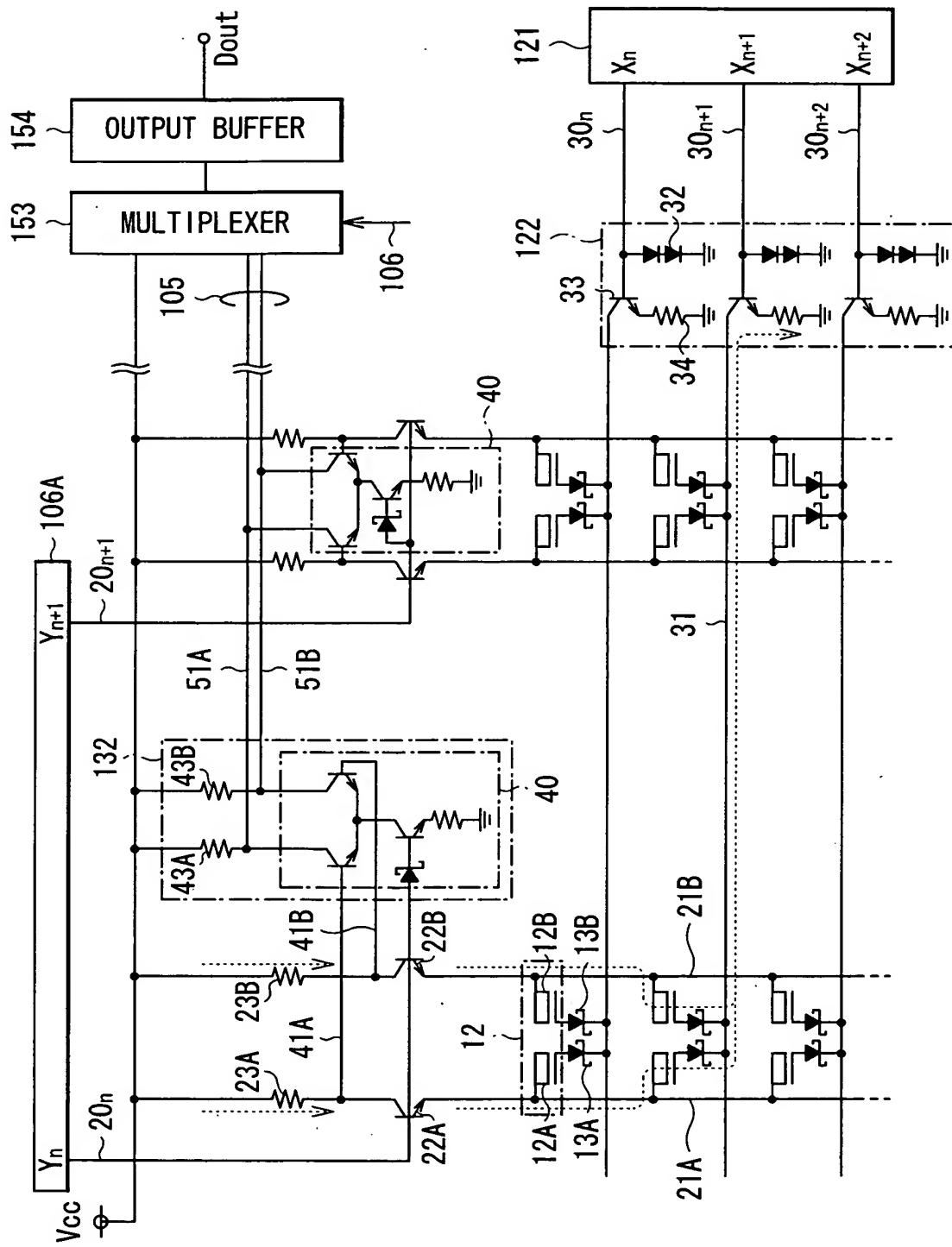


FIG. 17

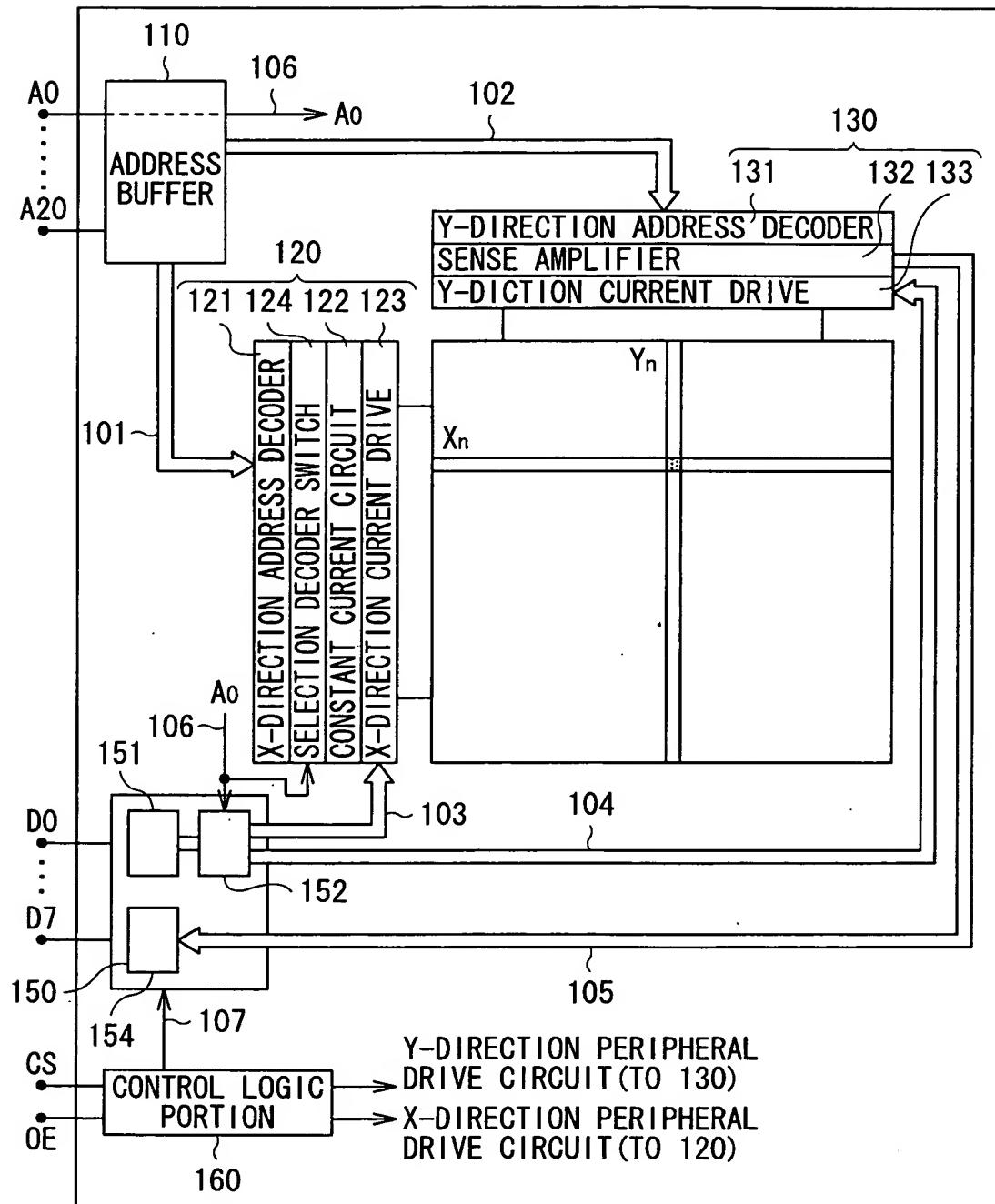


FIG. 18

18 / 29

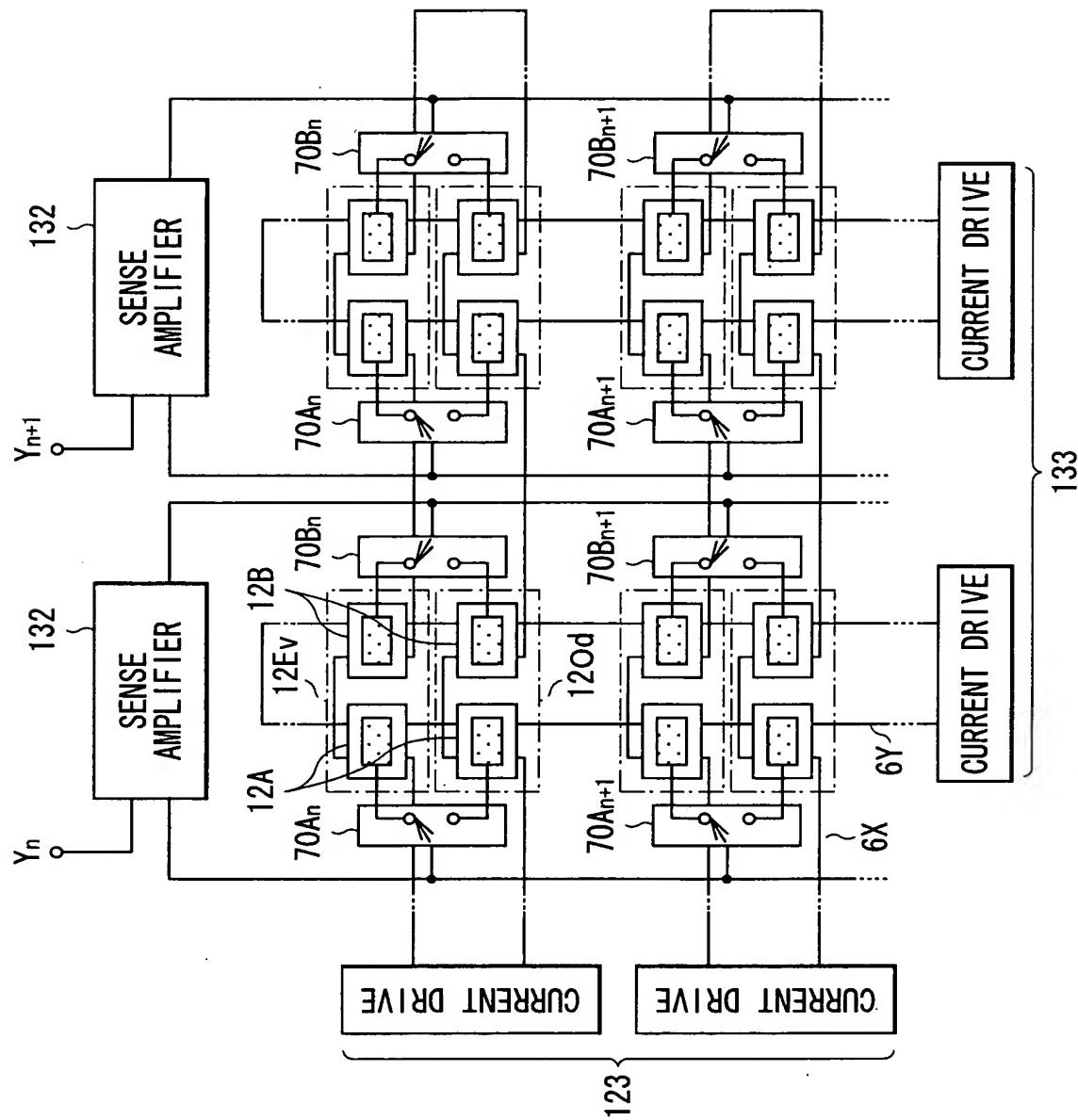


FIG. 19

19 / 29

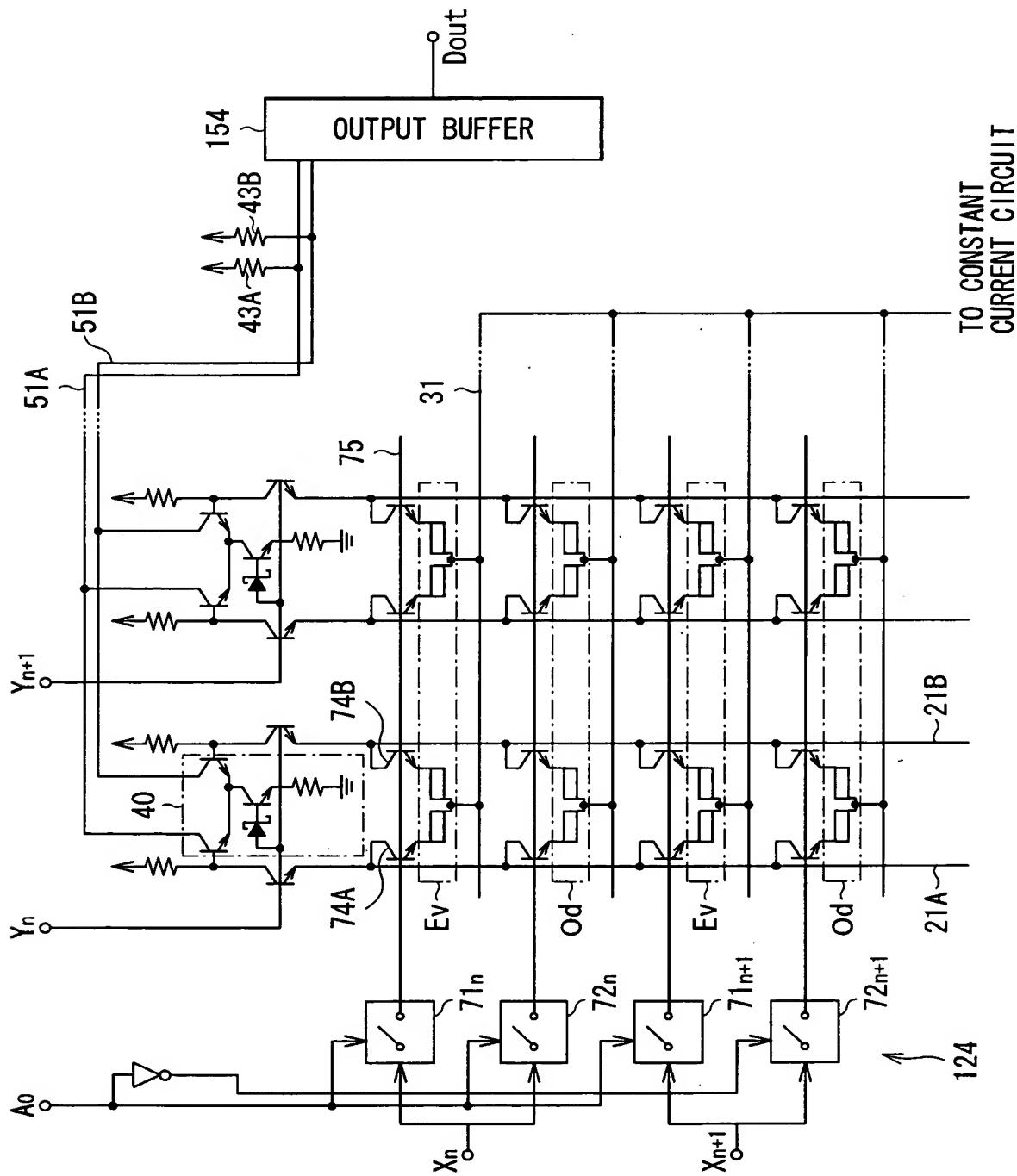


FIG. 20

20 / 29

FIG. 21A

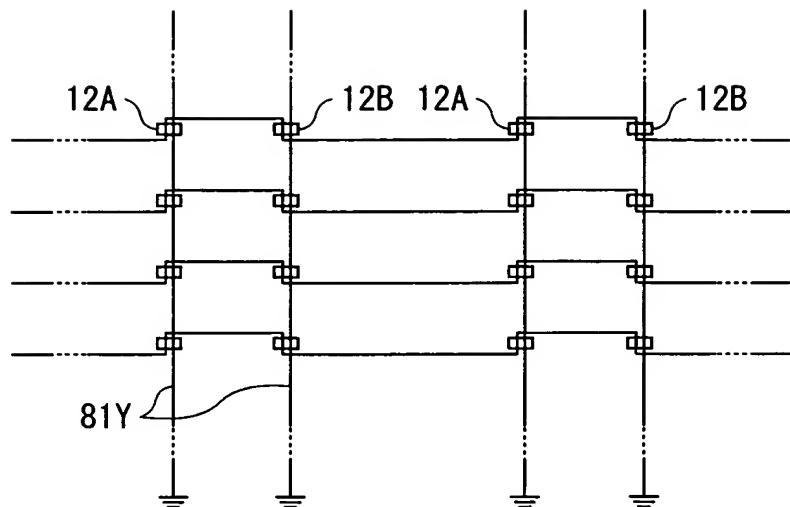
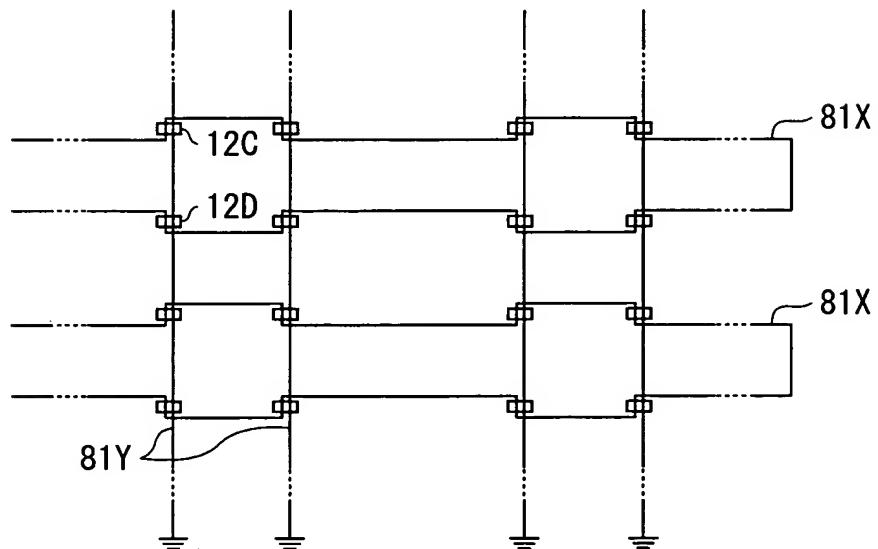
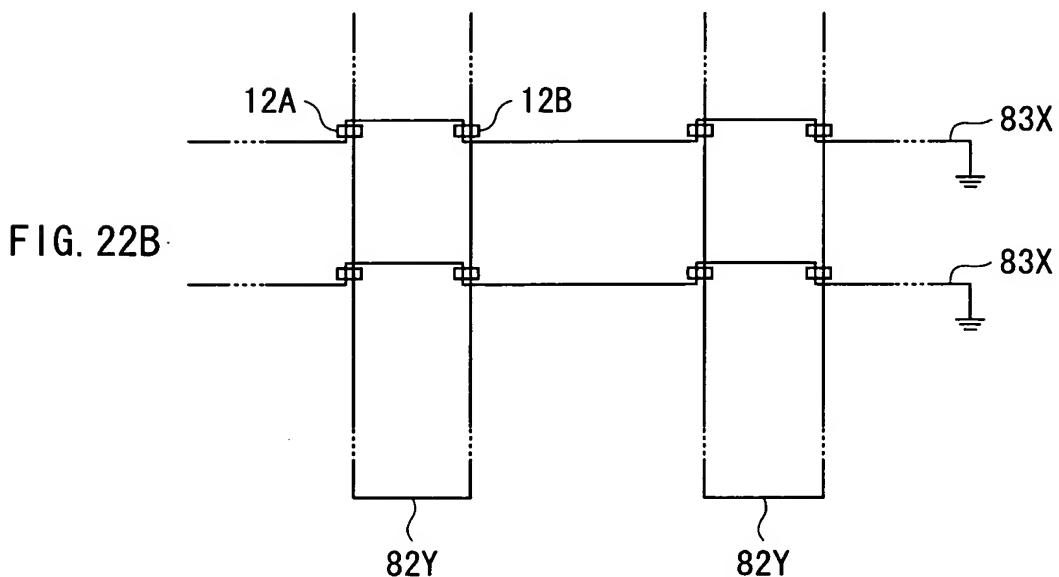
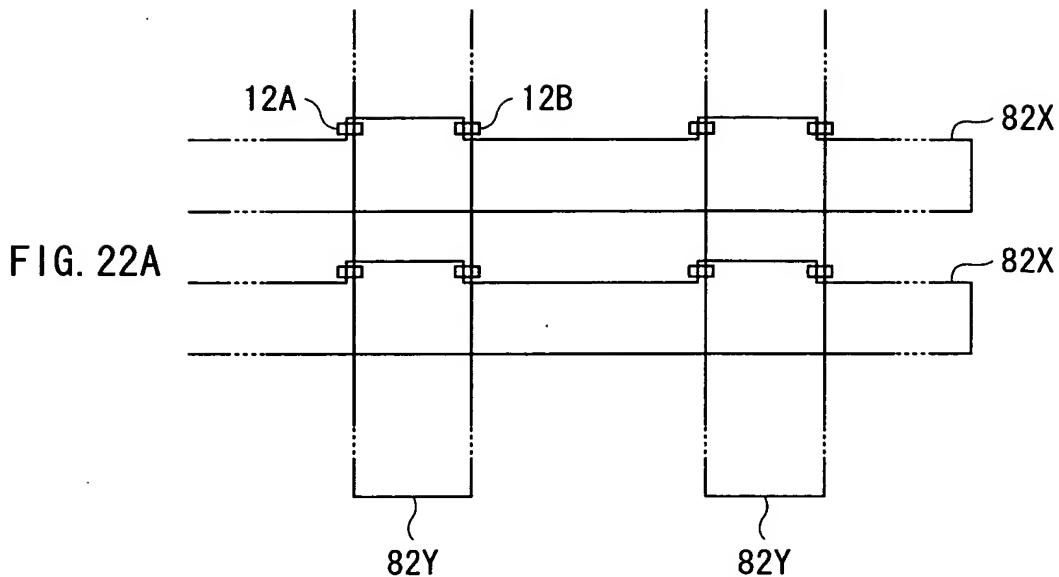


FIG. 21B



10/550201

21 / 29



22 / 29

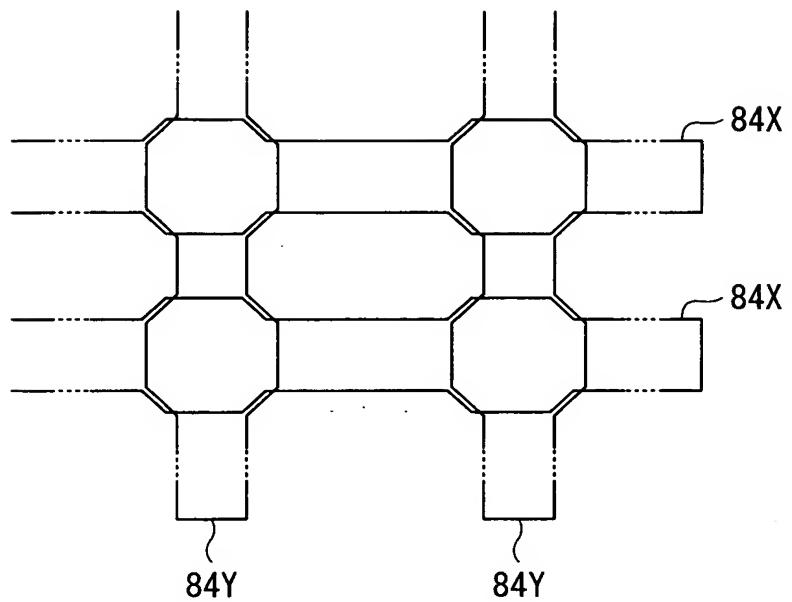


FIG. 23

23/29

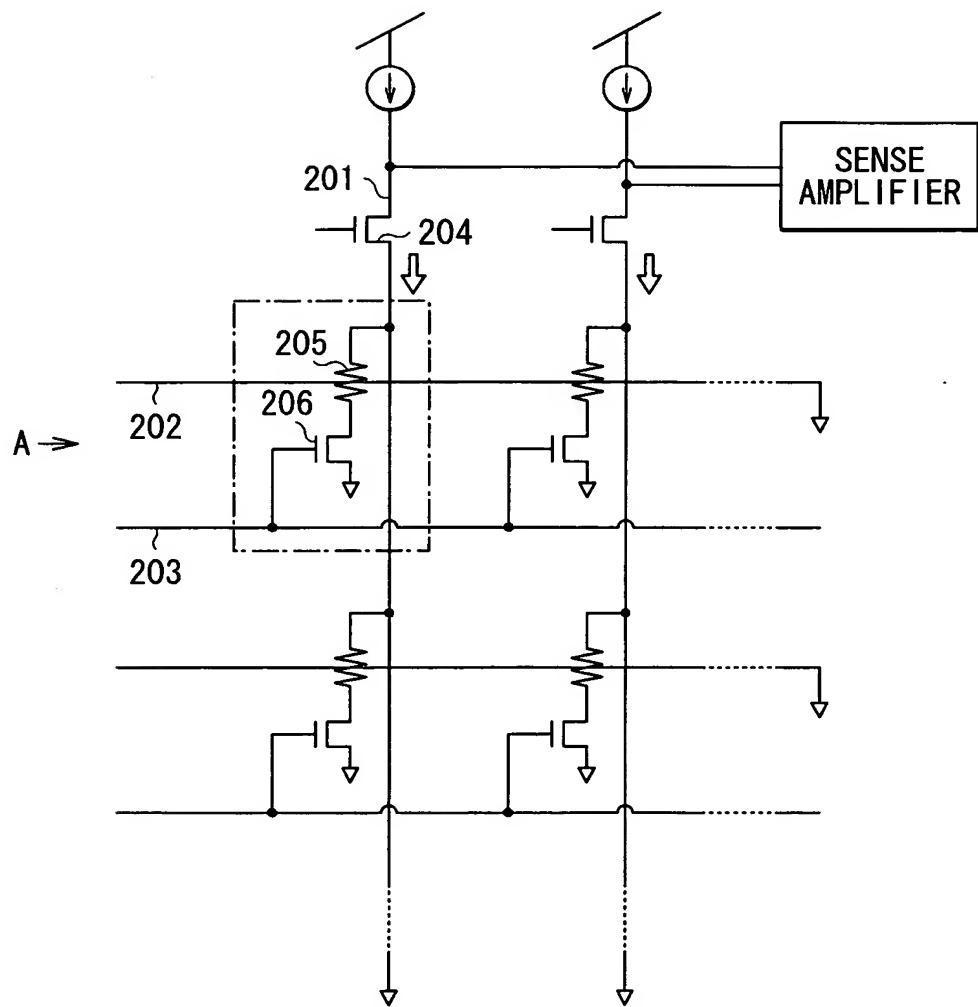


FIG. 24

24/29

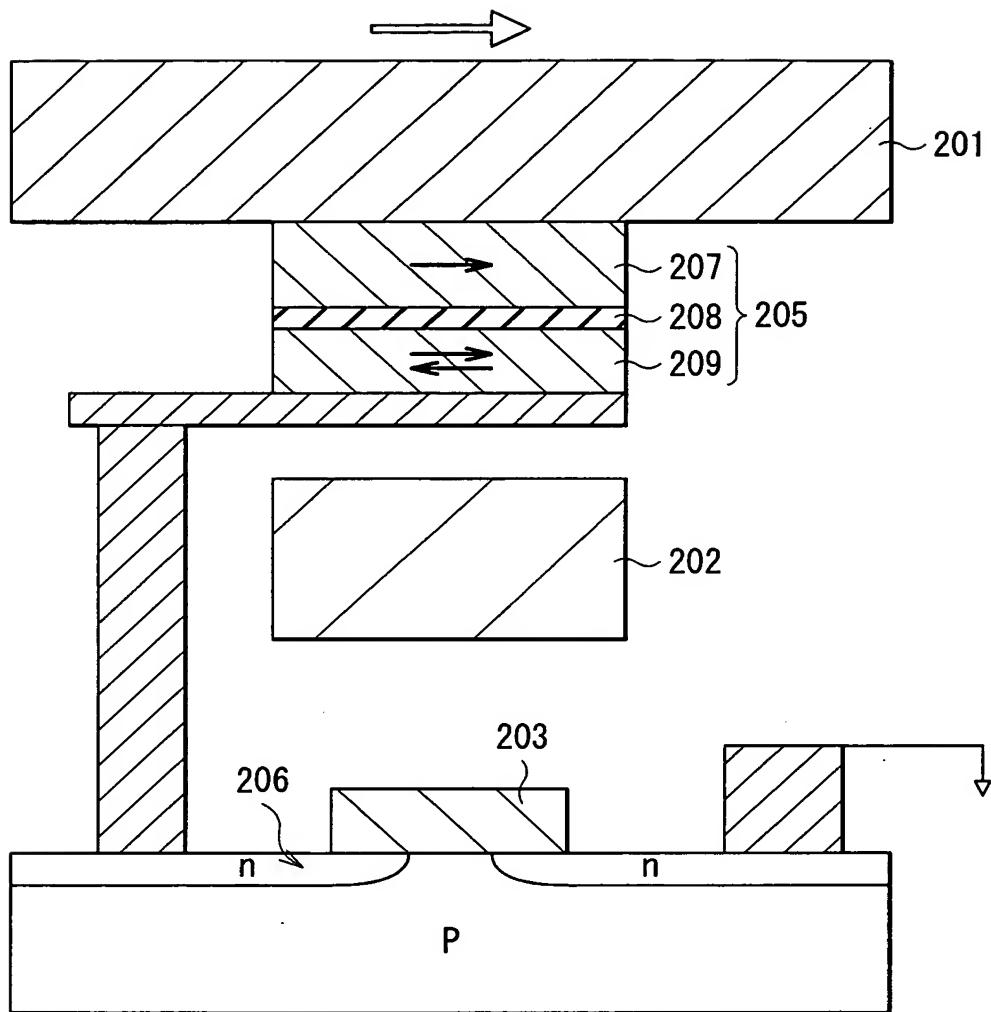


FIG. 25

25 / 29

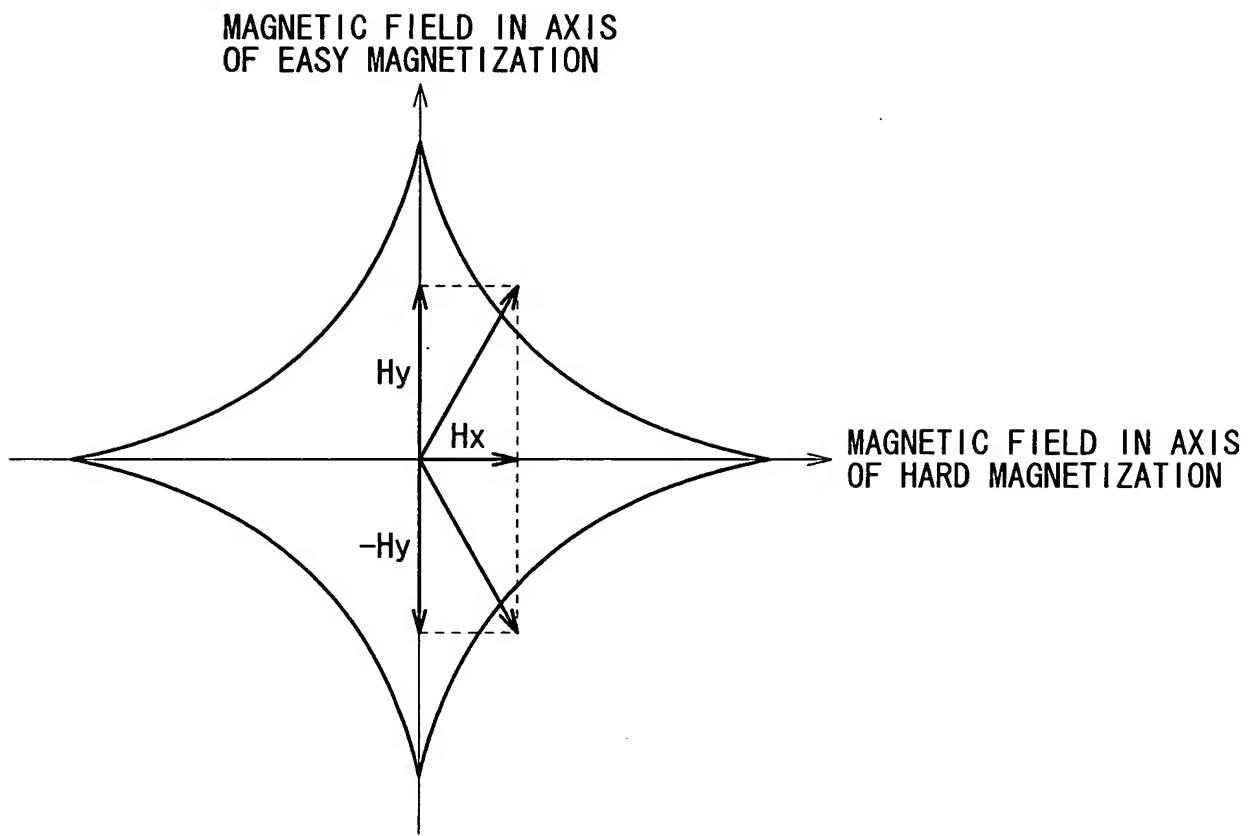


FIG. 26

26 / 29

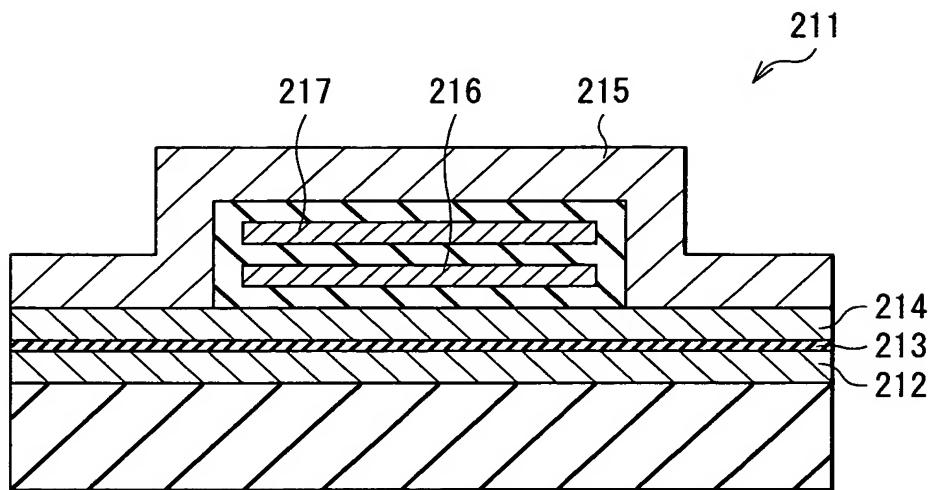


FIG. 27

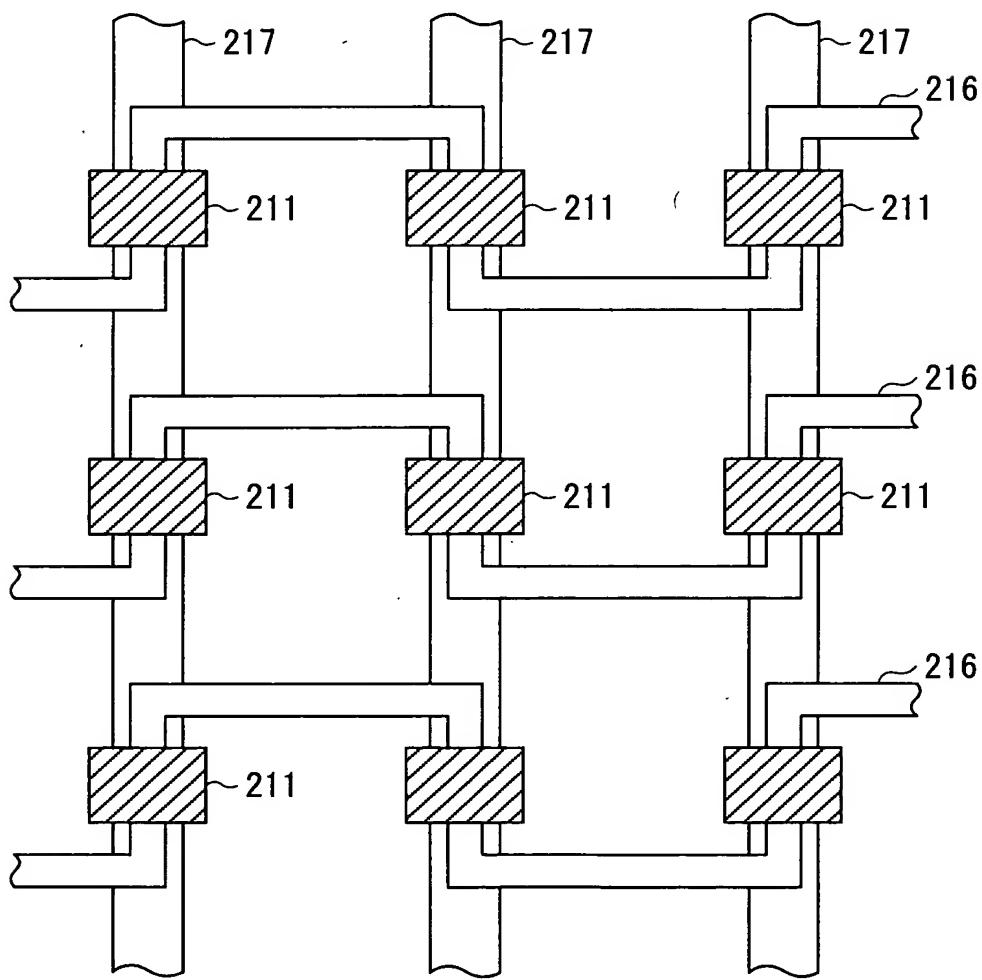


FIG. 28

10/550201

27 / 29

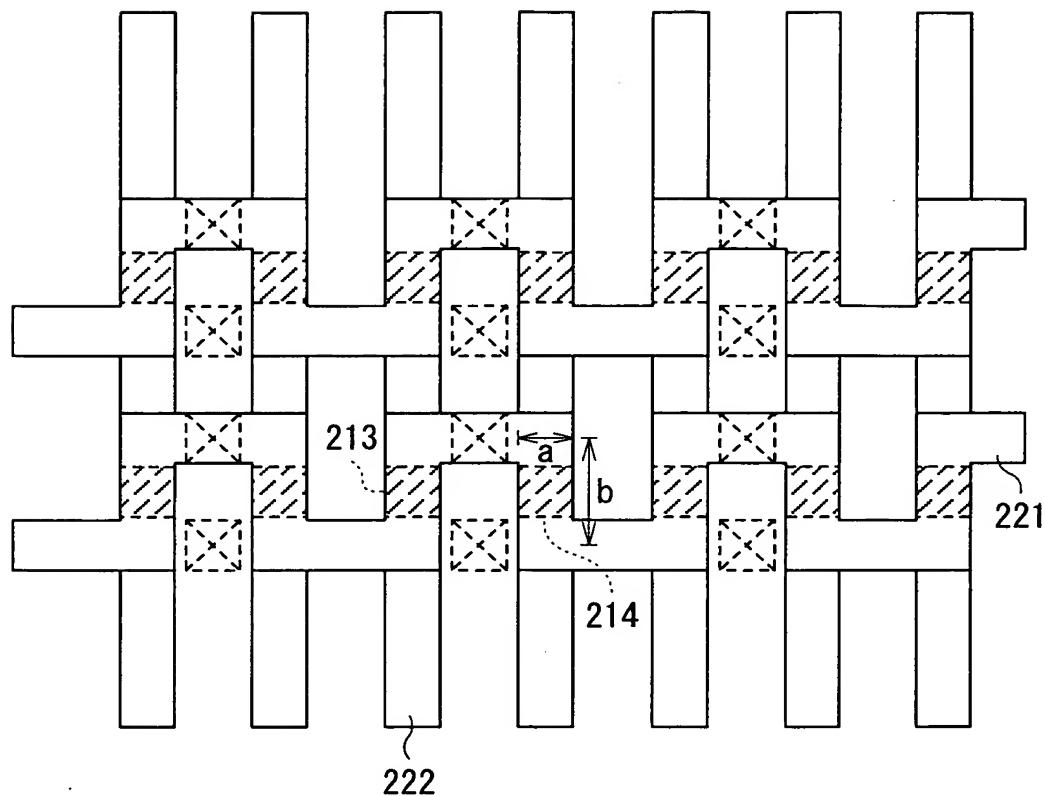


FIG. 29

28 / 29

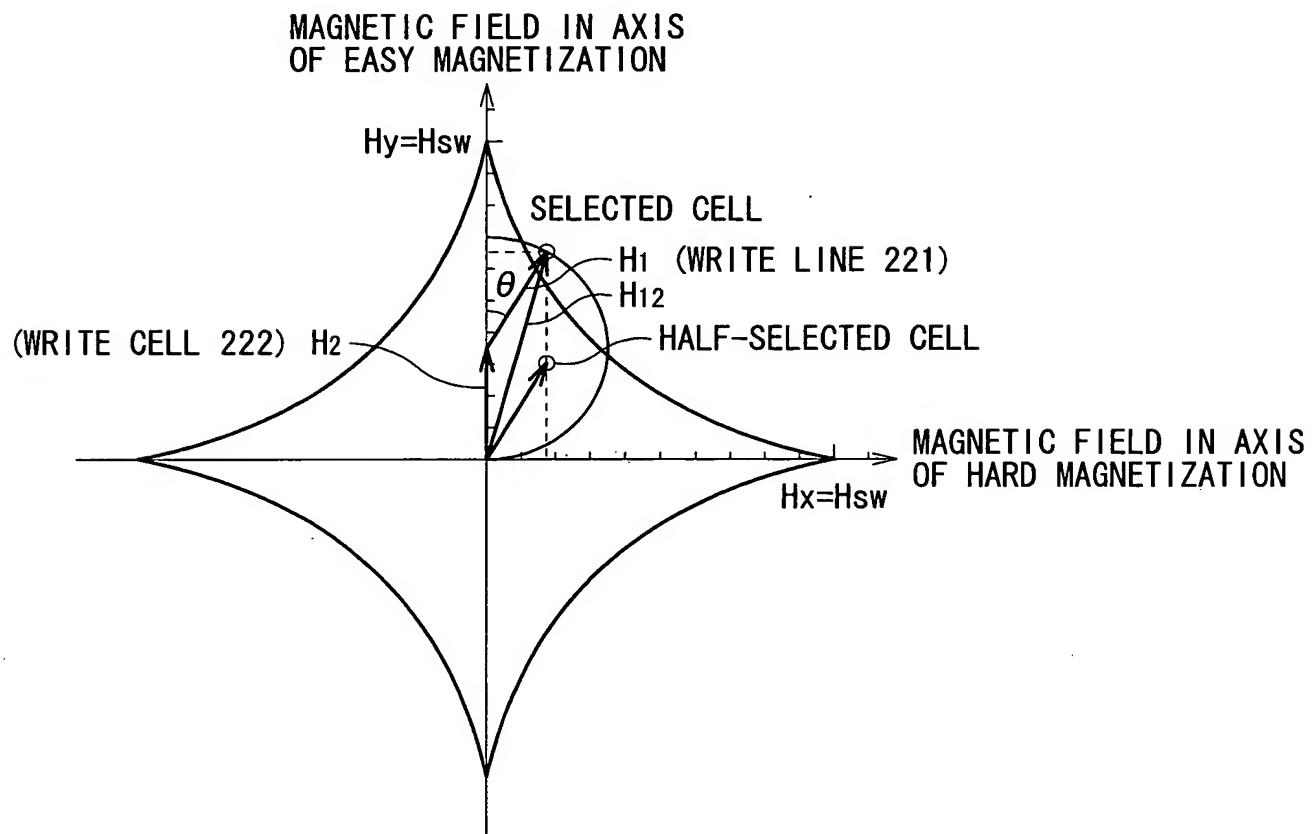


FIG. 30

10/550201

29 / 29

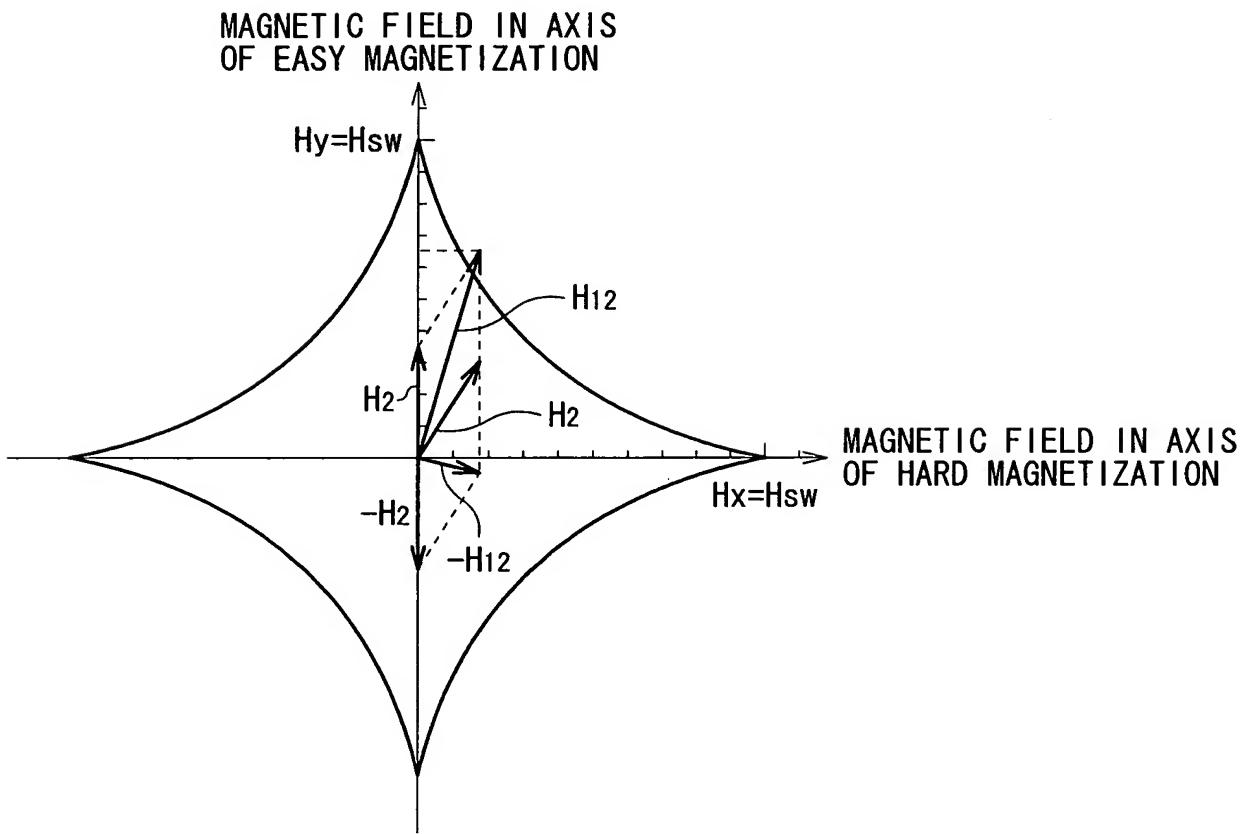


FIG. 31